EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1	"10/396118"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L4	1	10/665080	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L5	11	(counter with reverse with bit) with alternate$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L6	2461	455/296	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L7	1	(counter with reverse with cycle) with alternate$2 and EMI	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L8	295	375/287	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:07
L9	12	(counter with reverse with cycle) with alternate$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05

EAST Search History

L10	174	((control) with counter with (opposite or negative or invert$3)) with alternate$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L11	2	"4,528,662".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L12	2	"6,242,965".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L13	2	"6,317,476".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L14	4	"4,408,283".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L15	4	"4408283".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L16	2	"5612956".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05

EAST Search History

L17	2	((control) with counter with (opposite or negative or invert$3) with alternate$2 with (n adj bit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L18	1	((control) with counter with (opposite or negative or invert$3) with alternate$2 with (n adj bit)) and ((voltage) with oscillator) and emi	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L19	1	((control) with counter with (opposite or negative or invert$3) with alternate$2) and ((voltage) with oscillator) and emi	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L20	32	((control) with counter with (opposite or negative or invert$3) with alternate$2) and ((voltage) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L21	987	((control) with counter with (opposite or negative or invert$3)) and ((voltage) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L22	37	(((electromagnetic adj interference) or EMI) with (cancellation or reduction)) and ((control) with counter) and ((voltage) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L23	4	(((electromagnetic adj interference) or EMI) with (cancellation or reduction)) and ((control adj signal) with counter) and ((voltage adj control) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05

EAST Search History

L24	2	((electromagnetic adj interference adj cancellation)) and ((control adj signal) with counter) and ((voltage adj control) with oscillator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L25	11	("5731728"|"5736893"|"6107851"|"6229366"|"6249876").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L26	4	emi with (reduction or cancellation or reduce or cancel).ti. and counter and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L27	4014	375/346	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L28	1	L21 and L27	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L29	1268	713/501	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L30	44	Balakrishnan.in. and EMI	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05

EAST Search History

L31	133	emi with (reduction or cancellation or reduce or cancel).ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L32	2476	emi.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L33	1	L21 and L6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L34	1059	Balakrishnan.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L35	10	emi with (reduc$4 or cancellat$3).ti. and counter and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05
L36	0	L21 and L8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:08
L37	1	L21 and L29	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:05

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L38	2046	375/285	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:07
L39	0	L21 and L38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:08
L40	1	counter and (("n-bit" adj signal) with opposite) and (voltage near control) and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:10
L41	1	counter and (("n-bit" adj signal) same opposite) and (voltage near control) and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:10
L42	14	counter and (("n-bit" adj signal)) and (voltage near control) and oscillator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:12
L43	1	counter and (("n-bit" adj signal)) and (voltage near control) and oscillator and EMI	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:12
L44	1	(counter and (("n-bit" adj signal)) and (voltage near control) and oscillator).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:13

EAST Search History

L45	2	"6114915".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:14
L46	2	"5349309".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:14

Tip: Try removing quotes from your search to get more results.

EMI cancellation method and system - Patent 20040096020
The **oscillator** receives the voltage of the **voltage control** unit as a high ... cycle of the **counter**, the **n-bit signal** being counted by the n-bit **counter**. ...
www.freepatentsonline.com/20040096020.html - 36k - Cached - Similar pages

Programmable wide-range frequency synthesizer - Patent 6114915
Because VCO 106 is implemented by a programmable ring **oscillator**, the resynchronization between K **counter** 112 and M **counter** 110 becomes a challenging task. ...
www.freepatentsonline.com/6114915.html - 43k - Cached - Similar pages
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Second order phase locked loop - US Patent 5349309
The **voltage control** oscillating circuit which lower the gain of phase locked ... The **counter** 4 emits this count value to the adder 6 as the **n-bit signal**. ...
www.patentstorm.us/patents/5349309-description.html - 34k - Cached - Similar pages

In order to show you the most relevant results, we have omitted some entries very similar to the 3 already displayed.
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EMI cancellation method and system - Patent 20040096020
In an **EMI** canceller, a control signal generation unit includes a **counter**, ... The **oscillator** receives the voltage of the **voltage control** unit as a high ...
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Light sensing dimming control system for gas discharge lamps ...
a conversion circuit connected to the **voltage control oscillator** to receive ... The six **bit signal** created by the counting circuit 37 represents the desired ...
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[PDF] A 21-level (line-to-line) BTB system based on series connection of ...
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6-**bit signal**. The signal θ. 1lead. can be considered as the output. of a 24576 (= 48 × 2. 9.) step **counter**. As a result, taking the. upper six-bits of θ ...
ieeexplore.ieee.org/iel5/9338/29651/01348659.pdf - Similar pages

[PDF] Derwent
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depending on the combination of the output **bit signal**. As a result, the frequency. of the output clock signal (OUT) varies, thus decreasing **EMI** noise. ...
scientific.thomson.com/media/dw/productpdfs/itppdfs/itelpul.pdf - Similar pages

Matrixsynth: Sequencer
Dual x 20-Bit audio inputs for internal **oscillator**/filter/fx ... Multiple gestures are stored in different Banks and can be selected by **voltage control**. ...
matrixsynth.blogspot.com/.../label/Sequencer?updated-max=2007-01-18T21%3A04%3A00-08%3A00&max-results=20 - 250k - Supplemental Result - Cached - Similar pages

[PDF] Xilinx UG196 Virtex-5 RocketIO GTP Transceiver user guide
File Format: PDF/Adobe Acrobat
Using an external **oscillator** to drive GTP dedicated clock routing ... RXCHARISCOMMA is a two-**bit signal**. Bit 0 corresponds to the. lower byte of RXDATA, ...
www.xilinx.com/bvdocs/userguides/ug196.pdf - Similar pages

[PDF] Xilinx UG196 Virtex-5 RocketIO GTP Transceiver user guide
File Format: PDF/Adobe Acrobat
RXCHARISCOMMA is a two-**bit signal**. Bit 0 corresponds to the ... Provide AC coupling between the **oscillator** output pins and the Virtex-5 dedicated ...
direct.xilinx.com/bvdocs/userguides/ug196.pdf - Similar pages

LASER RECORDING SYSTEM WITH BOTH SURFACE DEFECT AND DATA ERROR ...
Thus, the **voltage control oscillator** and its feedback loop provide a frequency ... The segment verify **bit signal** associated with the segment bit is examined ...
www.wikipatents.com/3657707.html - 238k - Cached - Similar pages

Automatic fine tuning circuit - Patent # 4121254 - PatentGenius

If the frequency f.sub.l of the local **oscillator** is higher than it should be by an ... The highest significant **bit signal** from the **counter** 31 is A.sub.4, **...**
www.patentgenius.com/patent/4121254.html - 80k - Supplemental Result - Cached - Similar pages

In order to show you the most relevant results, we have omitted some entries very similar to the 9 already displayed.
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1. Light sensing dimming control system for gas discharge lamps
Adamson, Hugh Patrick / Langer, George O., *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Feb 2001
...circuit 18 comprises a voltage controlled **oscillator** (VCO) 23 (shown in FIG. 3) as part of...magnitude of the voltage signal so that the **voltage control** signal conducted to the VCO 23 is within...is in a particular state, a digital **counter** 37 actively counts clock pulses and...
Full text available at patent office. For more in-depth searching go to LexisNexis
view all 7 results from Patent Offices
similar results

2. HIGH DENSITY PERMANENT DATA STORAGE AND RETRIEVAL SYSTEM
UNITED KINGDOM PATENT APPLICATION, Mar 1973
...means may comprise a crystal controlled **oscillator**. In one embodiment of the invention...and wherein said voltage controlled **oscillator** is synchronized toa 55,the reference...reference clock and the voltage controlled **oscillator** is syn-chronized to the reference clock...
Full text available at patent office. For more in-depth searching go to LexisNexis
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3. LASER RECORDING UNIT
UNITED KINGDOM PATENT APPLICATION, Mar 1973
PATENT SPECIFICATION (11) 1309 102 DRAWINGS ATTACHED (21) Application No 12901/70 (22) Filed 17 March 1970 (311) Convention Application No 807 553 (32) Filed 17 Mlarch 1969 in (33) United States of America (US) (44) Complete Specification published 7 March 1973 (51) Internmudonal
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4. LASER RECORDING SYSTEM
UNITED KINGDOM PATENT APPLICATION, Mar 1973
PATENT SPECIFICATION DRAWINGS ATTACHED (21) Application No 12900/70 (22) Filed 17 Mareh 1970 (3,1) Convention Applicatiom No 807 548 i(32) Filed 17 March

1969 in (33) United States of America (US) (44) Complete Spedification published 7 March 1973 (51) Internatiomal Classificiaion Gli B 7/00
Full text available at patent office. For more in-depth searching go to LexisNexis
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☐ **5.** HIGH DENSITY PERMANENT DATA STORAGE AND RETRIEVAL SYSTEM
DELL HAROLD R / LARA EDWARD D / HASHIGUCHI MASAO, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jan 1972
...of a computing reading of data, and the synchronous clock, which may be a system or temporary storage. crystal-controlled **oscillator**, provides timing control signals It is an object of the present invention to provide a new and 10 for the sequencing logic...
Full text available at patent office. For more in-depth searching go to LexisNexis
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☐ **6.** LASER RECORDING SYSTEM USING DRUM MOUNTED RECORD STRIPS
MCFARLAND KEITH E / BECKER CARL H / DELL HAROLD R / WONG HERMAN / FRENCH BALLARD D / HASHIGUCHI MASAO, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Apr 1972
A laser recording system for scanning a modulated laser beam in spaced parallel lines across a flat elongated strip of energy absorbing material and for ablating minute regions from the material in a linear pattern representing digital bits according to ...
Full text available at patent office. For more in-depth searching go to LexisNexis
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☐ **7.** LASER RECORDING SYSTEM WITH BOTH SURFACE DEFECT AND DATA ERROR CHECKING

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Apr 1972
This invention relates to a new and improved laser record- ablates in the form of data tracks, an energy-absorbing inforing system for permanently recording digital data at high den- 5 mation storage medium during recording. Digital information sity on an energy-absorbing information storage medium
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Sort by:: :**relevance** | **date**

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Al

1. Phase locked loop
Fujii, Takashi, *EUROPEAN PATENT APPLICATION*, Oct 1993
...up-down **counter**. This **counter** 4, if the UP is entered...the internal up-down **counter** operates by +1, if the...changed any more. The **counter** 4 emits this count value to the adder 6 as the **n-bit signal**. On the other hand...circuit comprises a quartz **oscillator** 34, an amplifier 35...
Full text available at patent office. For more in-depth searching go to LexisNexis
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2. Programmable wide-range frequency synthesizer
Huang, Joseph / Wang, Xiaobao / Sung, Chiakang / Wang, Bonnie I. / Nguyen, Khai / Yeung, Wayne / Kim, In Whan, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Sep 2000
...stages of the ring **oscillator**. Multiplexer 302-32...third-second stage of the ring **oscillator** or the output of the...stages of the ring **oscillator**. A critical aspect...speed at which these **counters** operate is a major...MUX 406 receives an **N-bit signal** from a cycle selection...
Full text available at patent office. For more in-depth searching go to LexisNexis
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3. METER INTERROGATION SYSTEMS
UNITED KINGDOM PATENT APPLICATION, Oct 1975
PATENT SPECIFICATION (21) Application No 23069/73 (22) Filed 15 May 1973 (44) Complete Specification published 8 October 1975 (51) INT CL 2 H 04 Q 9/00 f I GO 8 C 19/14 (52) Index at acceptance G 4 H 12 G 13 D 14 X 23 D 7 A 3 7 B 13 F 23 G 7 L 14 A 3 X NC 2 14 B 14 D 6 B 6 F (54) METER
Full text available at patent office. For more in-depth searching go to LexisNexis
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4. METER INTERROGATION SYSTEM HAVING STROBE LOGIC CONTROL
LUSK J / ROOD W / FELDMAN H, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, May 1973

...problems enthat make up a central control unit and a transponder **countered** in transmitting and receiving data from cenunit. 1 5 tral control...reverts of comparison unit shifts the contents of register **counter** 73 to its A state. Interval decoder 74 may be 112 to modem...
Full text available at patent office. For more in-depth searching go to LexisNexis
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☐ **5.** Second order phase locked loop
Fujii, Takashi, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Sep 1994
...changed any more. The **counter** 4 emits this count...adder 6 as the **n-bit signal**. On the other hand...emits 0, as the **n-bit signal**. The adder 6 adds the output of the **counter** 4 and the output...terminals of the quartz **oscillator** 34; load capacitors...
Full text available at patent office. For more in-depth searching go to LexisNexis
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☐ **6.** METER INTERROGATION SYSTEMS
UNITED KINGDOM PATENT APPLICATION, Oct 1975
(21) Application No 1159/75 (62) (44) (51) (22) Filed 15 May 1973 Divided out of No 1409489 Complete Specification published 8 October 1975 INT CL 2 H 04 Q 9/00 11 GO 8 C 19/14 (52) Index at acceptance G 4 H 12 G 13 D 13 F 14 A 14 B 14 D 14 X 23 D 23 G 3 X 6 B 6 F 7 A 3 7 B 7 L NC 2 (72)
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IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
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IEEE STD	IEEE Standard

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1. **The architecture of delta sigma analog-to-digital converters using a volta oscillator as a multibit quantizer**
Iwata, A.; Sakimura, N.; Nagata, M.; Morie, T.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactic Circuits and Systems II: Express Briefs, IEEE Transactions on]
Volume 46, Issue 7, July 1999 Page(s):941 - 945
Digital Object Identifier 10.1109/82.775391
AbstractPlus | References | Full Text: PDF(208 KB) IEEE JNL
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2. **Study of 1 MHz /spl sim/ 30 MHz voltage-controlled LC oscillator**
Xiaohui Ye; Qiang Wu; Zheng Euang; Yong Liu;
Radio Science Conference, 2004. Proceedings. 2004 Asia-Pacific
24-27 Aug. 2004 Page(s):280 - 282
Digital Object Identifier 10.1109/APRASC.2004.1422459
AbstractPlus | Full Text: PDF(1761 KB) IEEE CNF
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3. **A wide tuning range gated voltage-controlled oscillator**
Bowman, T.G.; Weiss, F.G.;
Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 1989. Technical Di Annual
22-25 Oct. 1989 Page(s):197 - 200
Digital Object Identifier 10.1109/GAAS.1989.69325
AbstractPlus | Full Text: PDF(316 KB) IEEE CNF
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4. **Dual-chip GaAs monolithic integration Ku-band phase-locked-loop micro synthesizer**
Ohira, T.; Muraguchi, M.; Hirota, T.; Osafune, K.; Ino, M.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 38, Issue 9, Sept. 1990 Page(s):1204 - 1209
Digital Object Identifier 10.1109/22.58644
AbstractPlus | Full Text: PDF(572 KB) IEEE JNL
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5. **Comments on `State-preserving intermittently locked loop (SPILL) freque for portable radio' [and reply]**

Thiel, D.V.; Saito, S.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 38, Issue 10, Oct. 1990 Page(s):1532
Digital Object Identifier 10.1109/22.58701

AbstractPlus | Full Text: PDF(80 KB) IEEE JNL
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6. **A 14-GHz 256/257 dual-modulus prescaler with secondary feedback and i a monolithic CMOS 10.4-GHz phase-locked loop**
Dong-Jun Yang; O, K.K.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 52, Issue 2, Feb. 2004 Page(s):461 - 468
Digital Object Identifier 10.1109/TMTT.2003.821918

AbstractPlus | References | Full Text: PDF(648 KB) IEEE JNL
Rights and Permissions

7. **Structural fault diagnosis in charge-pump based phase-locked loops**
Medury, A.; Carlson, I.; Alvandpour, A.; Stensby, J.;
VLSI Design, 2005. 18th International Conference on
3-7 Jan. 2005 Page(s):842 - 845
Digital Object Identifier 10.1109/ICVD.2005.153

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8. **A 2 GHz, 6 mW BiCMOS frequency synthesizer**
Aytur, T.; Razavi, B.;
Solid-State Circuits Conference, 1995. Digest of Technical Papers. 42nd ISSC International
15-17 Feb. 1995 Page(s):264 - 265
Digital Object Identifier 10.1109/ISSCC.1995.535549

AbstractPlus | Full Text: PDF(180 KB) IEEE CNF
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9. **A Ku-band MMIC PLL frequency synthesizer**
Ohira, T.; Muraguchi, M.; Hirota, T.; Osafune, K.; Ino, M.;
Microwave Symposium Digest, 1989., IEEE MTT-S International
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09643534	6381151	150	08/22/2000	HIGH EFFICIENCY SWITCHING CONTROLLER	JANG, KYUNG-OUN
09761902	6507080	150	01/17/2001	MOS TRANSISTOR AND FABRICATION METHOD THEREOF	JANG, KYUNG-OUN
10300293	6853040	150	11/20/2002	MOS TRANSISTOR AND FABRICATION METHOD THEREOF	JANG, KYUNG-OUN
10665080	Not Issued	41	09/16/2003	EMI cancellation method and system	JANG, KYUNG-OUN
10698056	6972971	150	10/29/2003	PULSE WIDTH MODULATION SIGNAL GENERATOR AND SWITCHING MODE POWER SUPPLY INCLUDING THE SAME	JANG, KYUNG-OUN
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10698056	6972971	150	10/29/2003	PULSE WIDTH MODULATION SIGNAL GENERATOR AND SWITCHING MODE POWER SUPPLY INCLUDING THE SAME	KIM, EUNG-SUEN
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